

000000000000000000000000

FIG. 1A

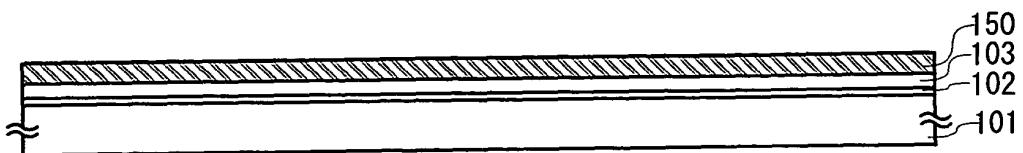


FIG. 1B

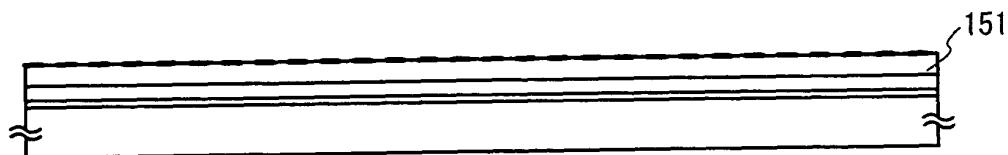


FIG. 1C

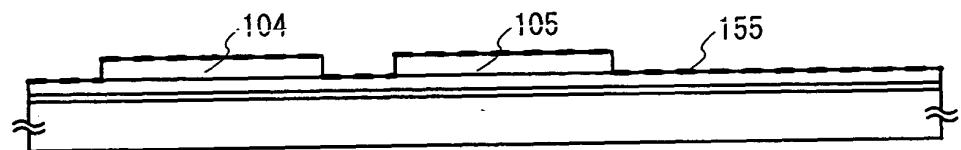


FIG. 1D

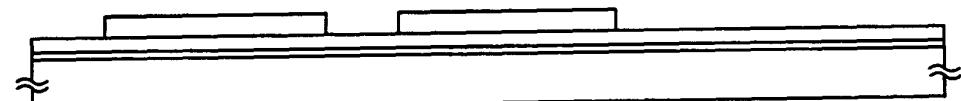


FIG. 1E

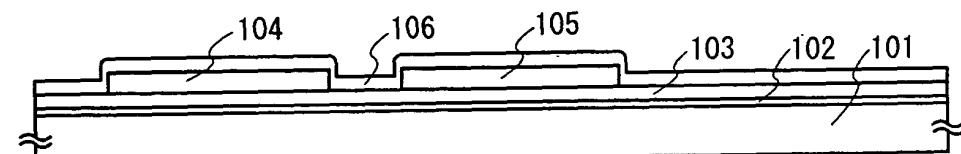
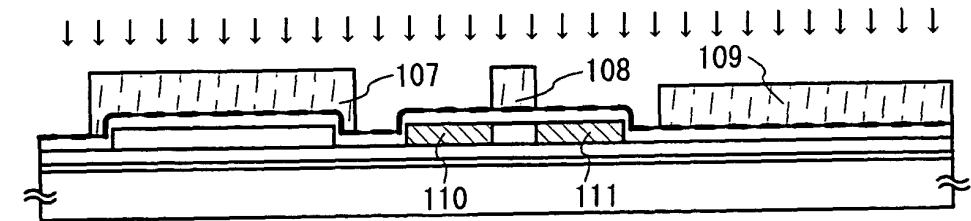
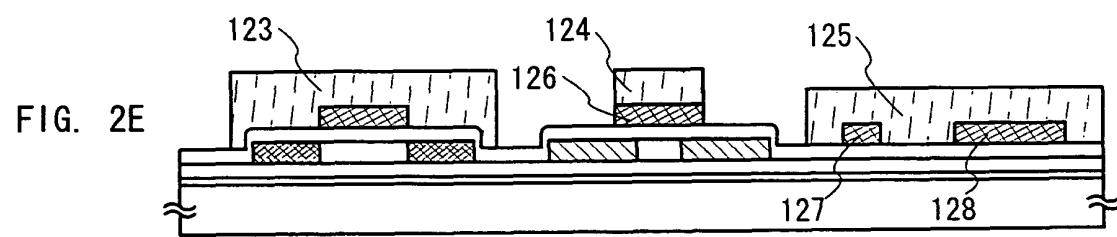
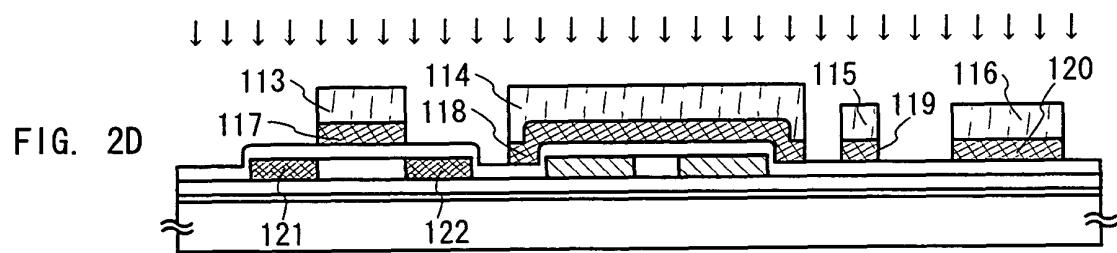
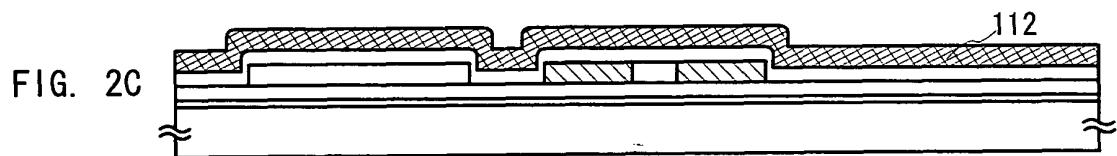
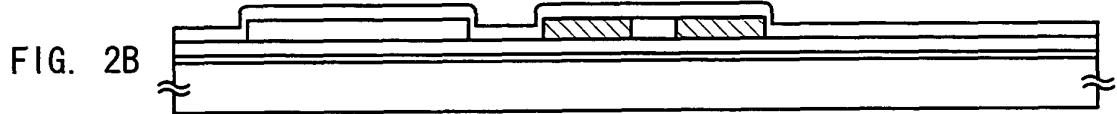
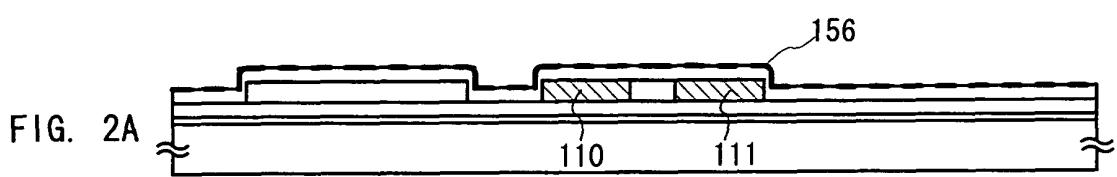


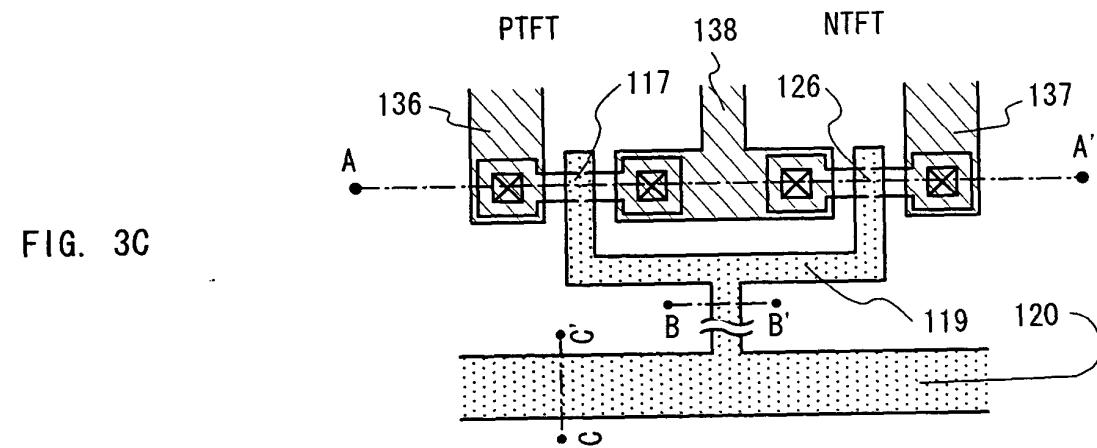
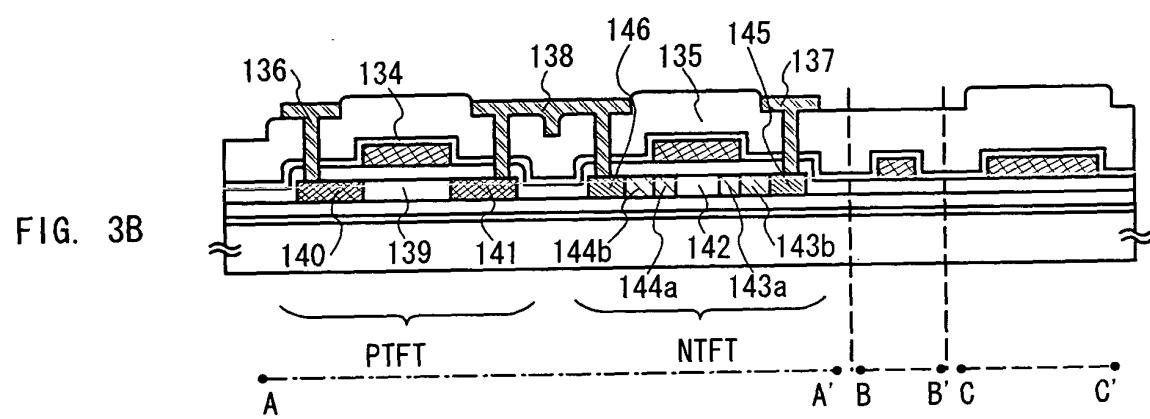
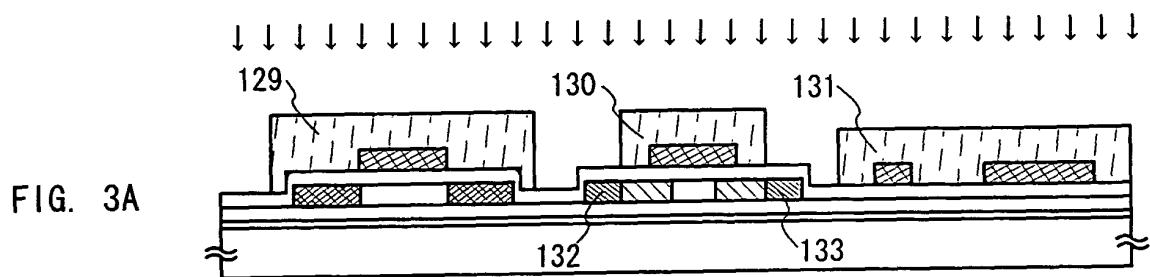
FIG. 1F



2013022260



003120 2020061600



© 1982 IEEE - 22nd Annual Proceedings

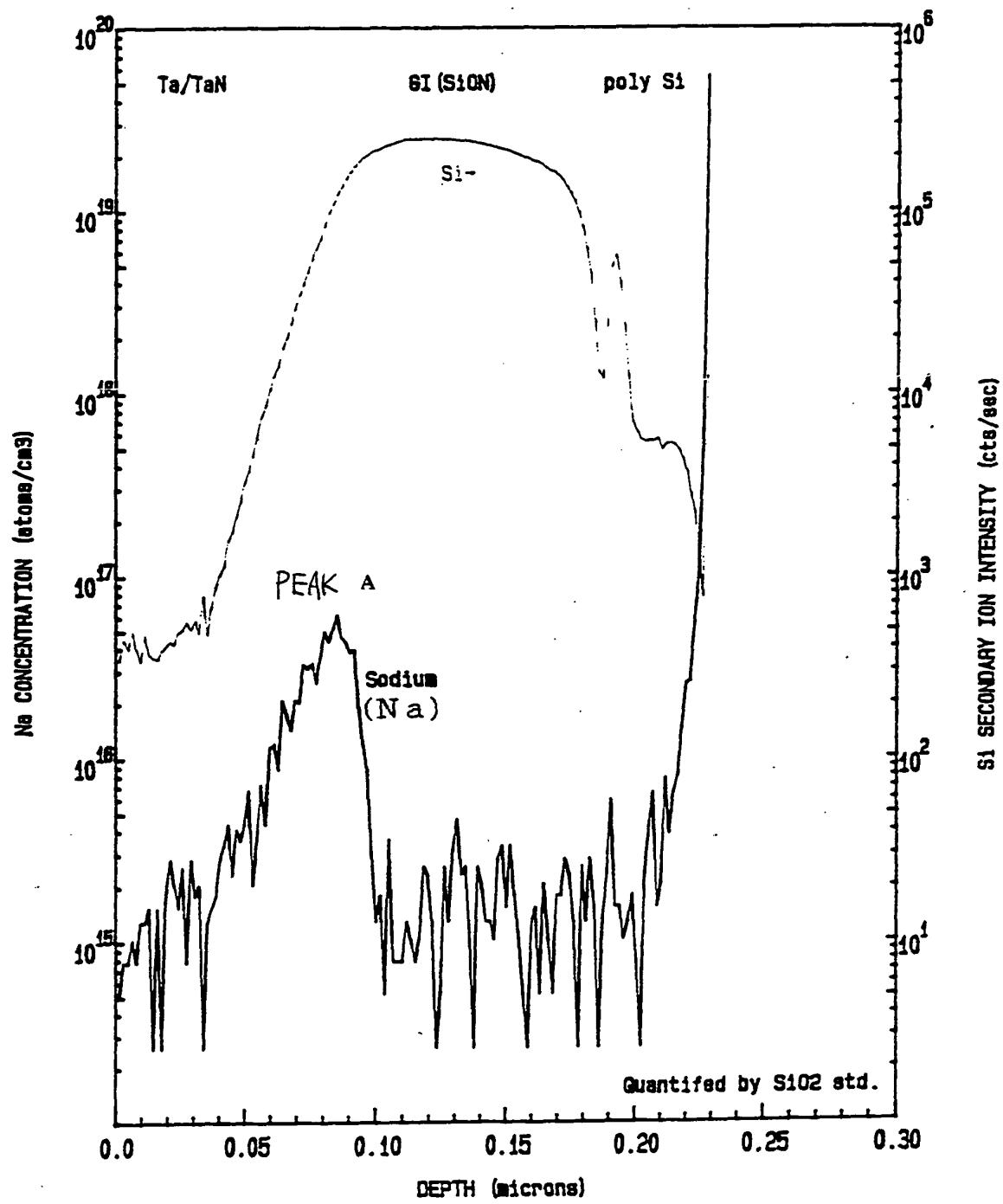


FIG. 4

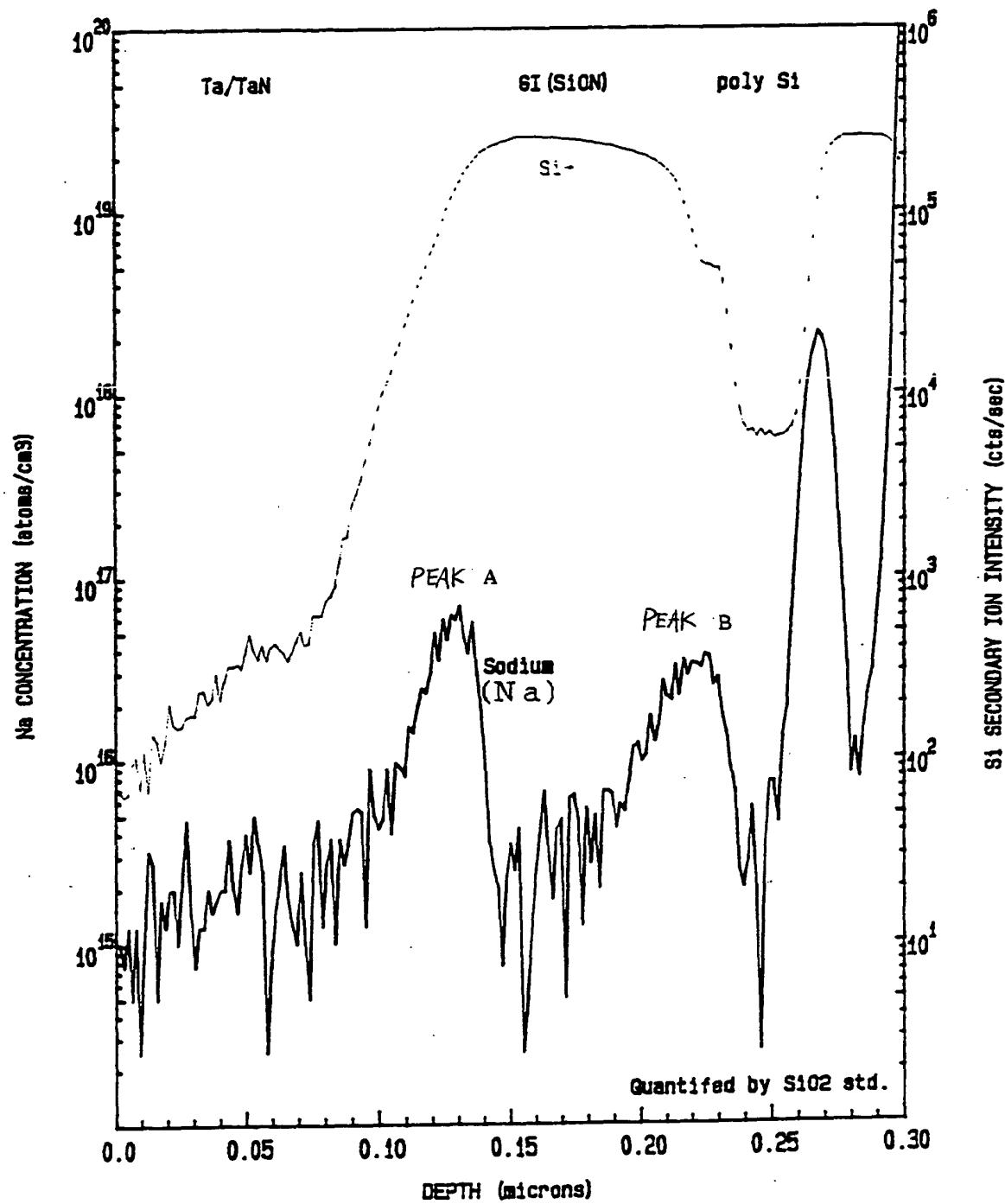


FIG. 5

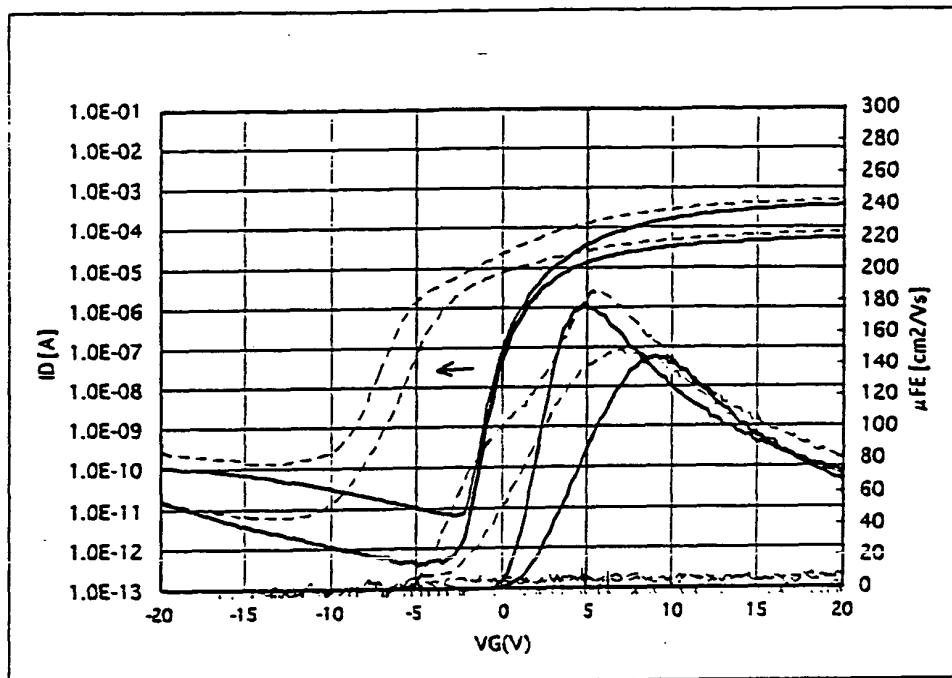


FIG. 6A

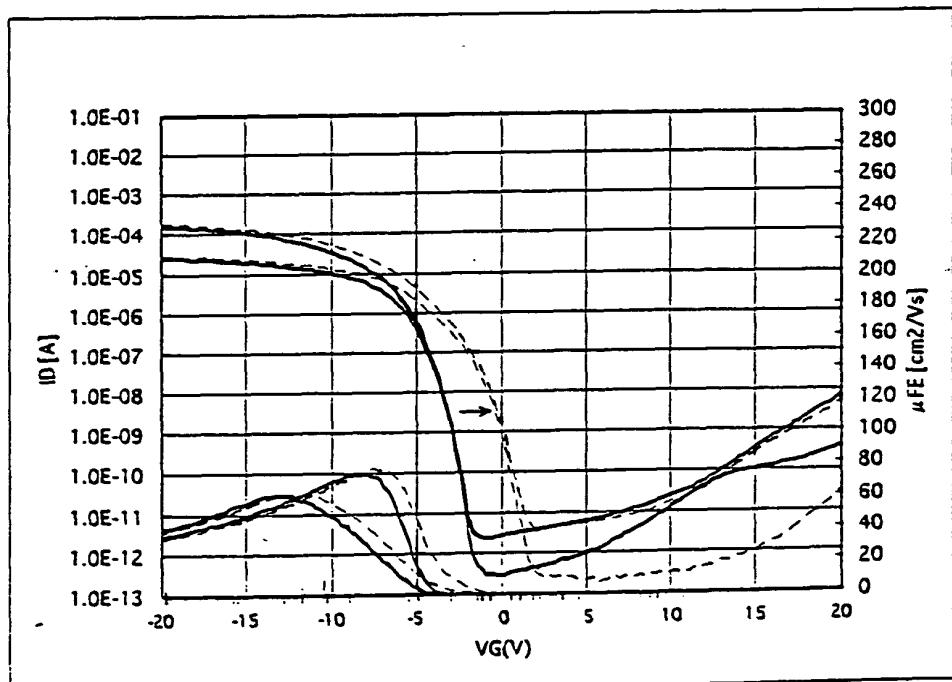


FIG. 6B

© 1994 Cambridge University Press

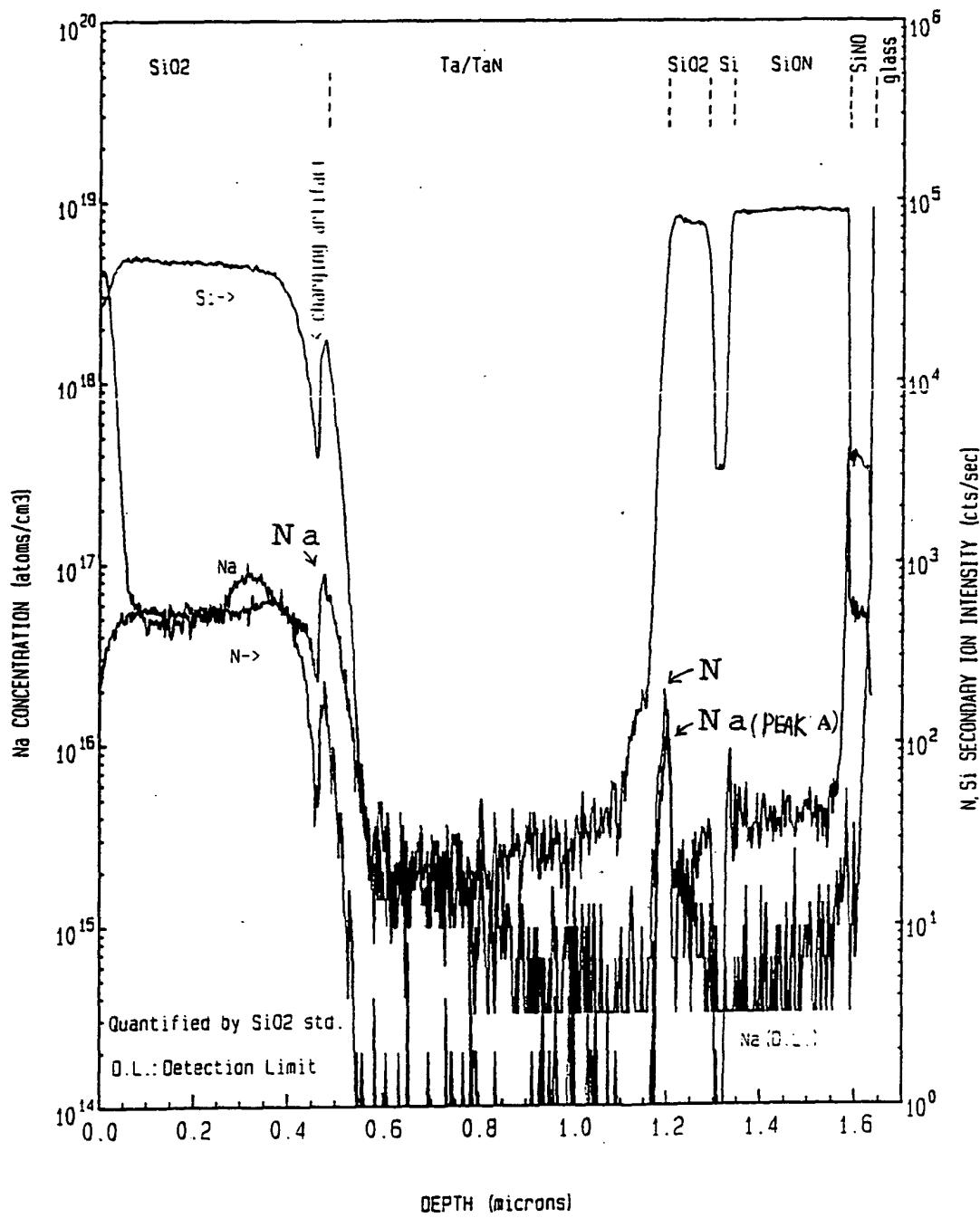


FIG. 7

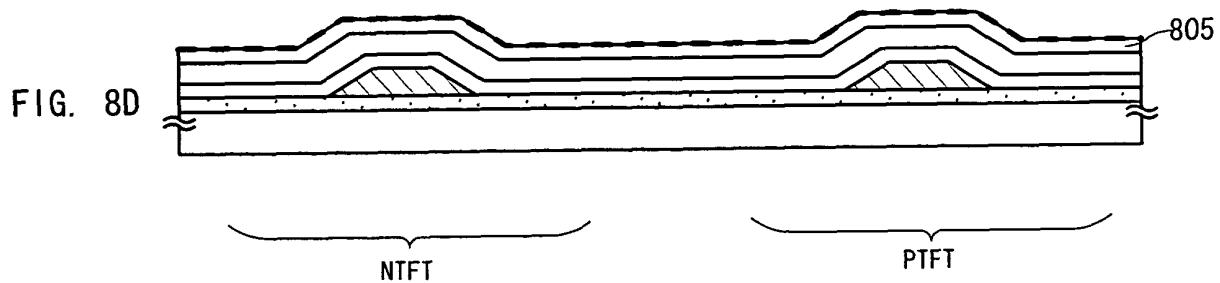
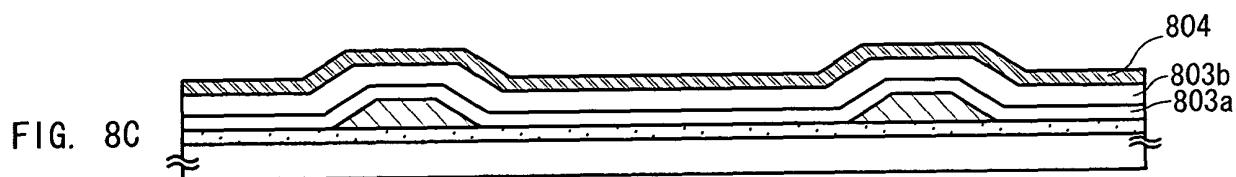
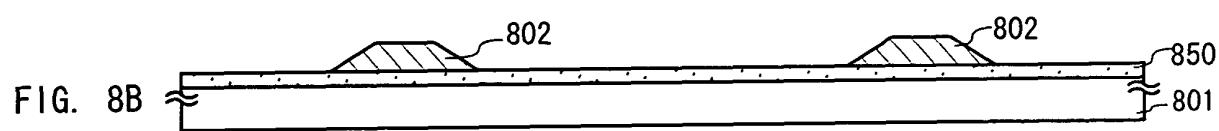
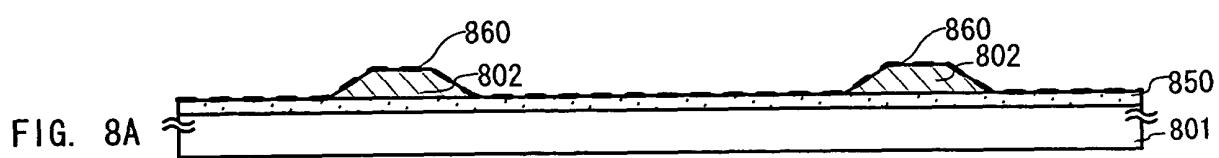


FIG. 9A

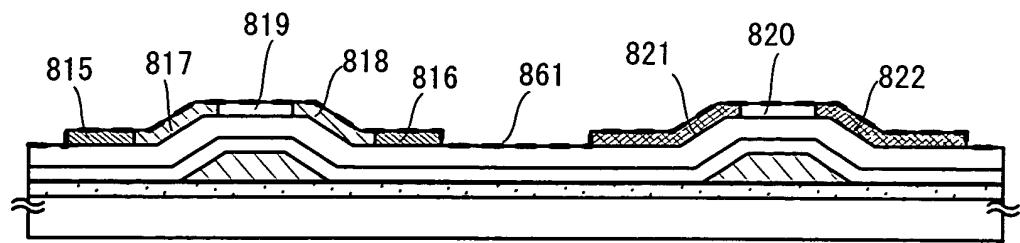


FIG. 9B

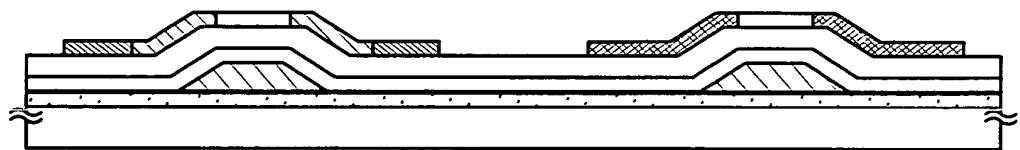


FIG. 9C

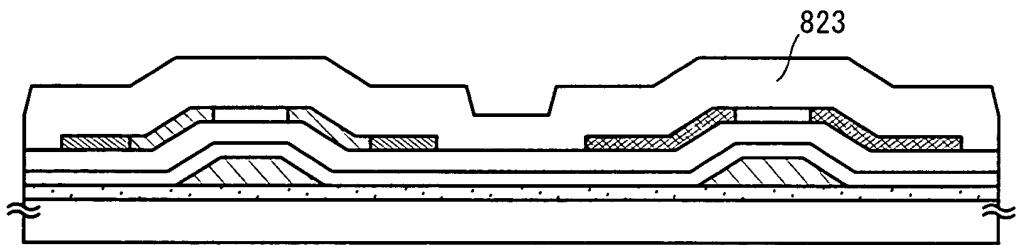
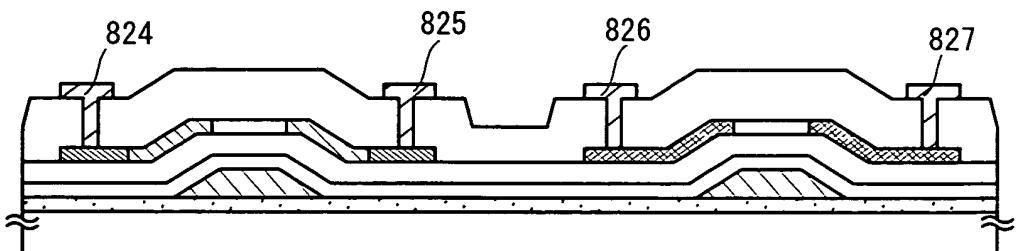
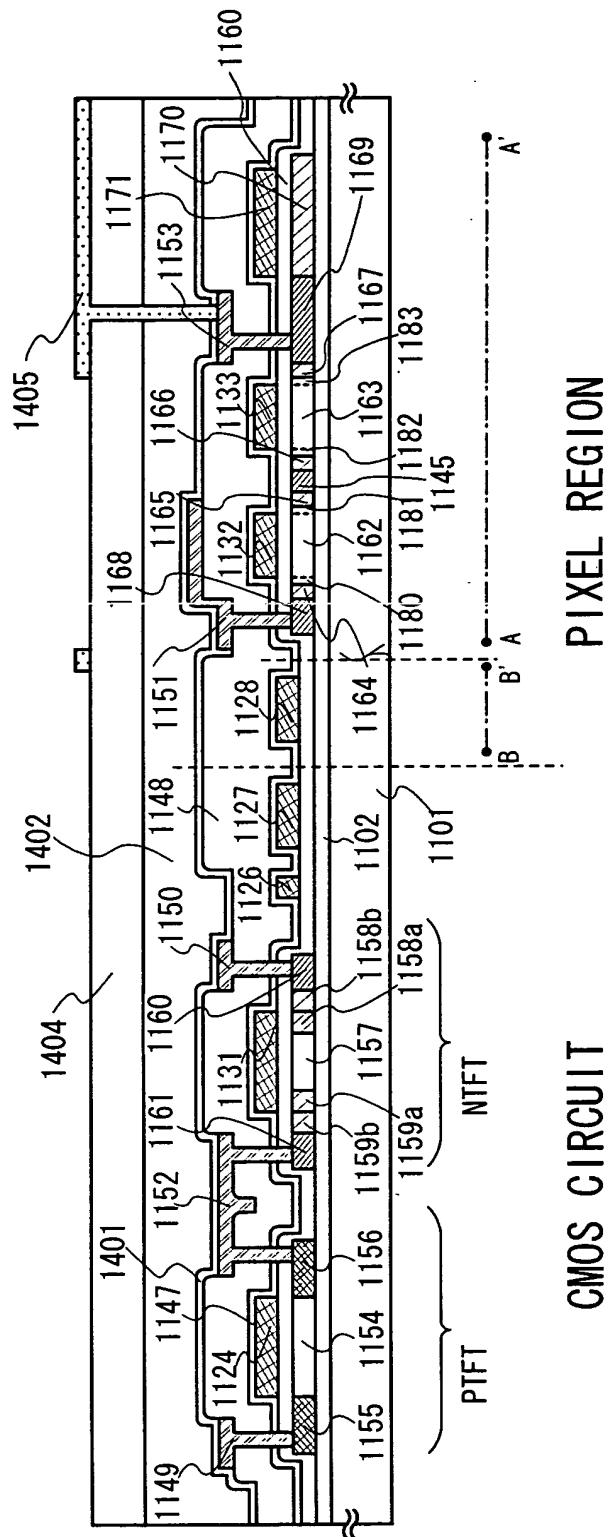


FIG. 9D



NTFT

PTFT



CMOS CIRCUIT
PIXEL REGION

FIG. 10

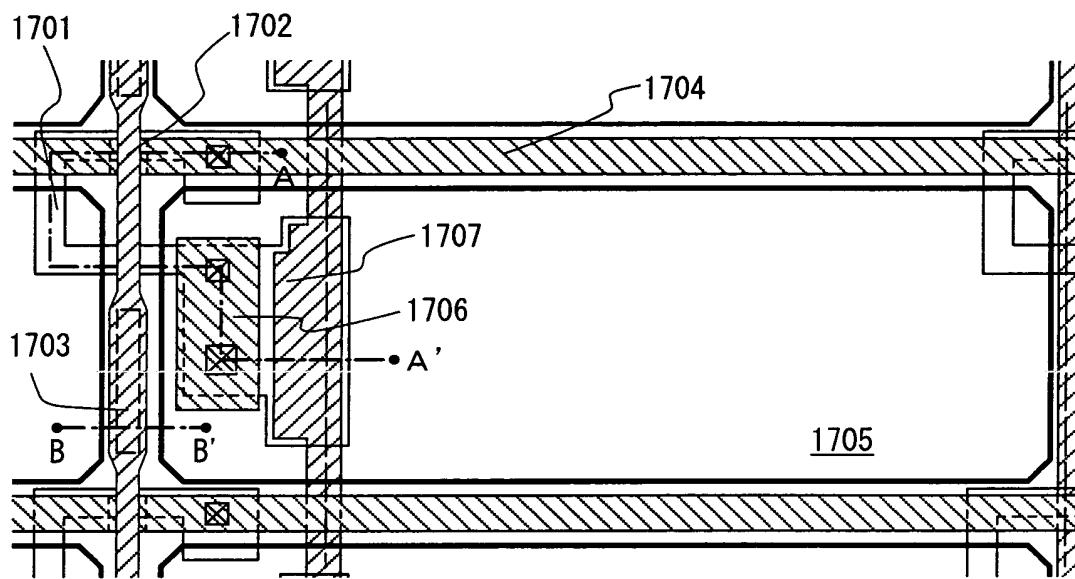


FIG. 11

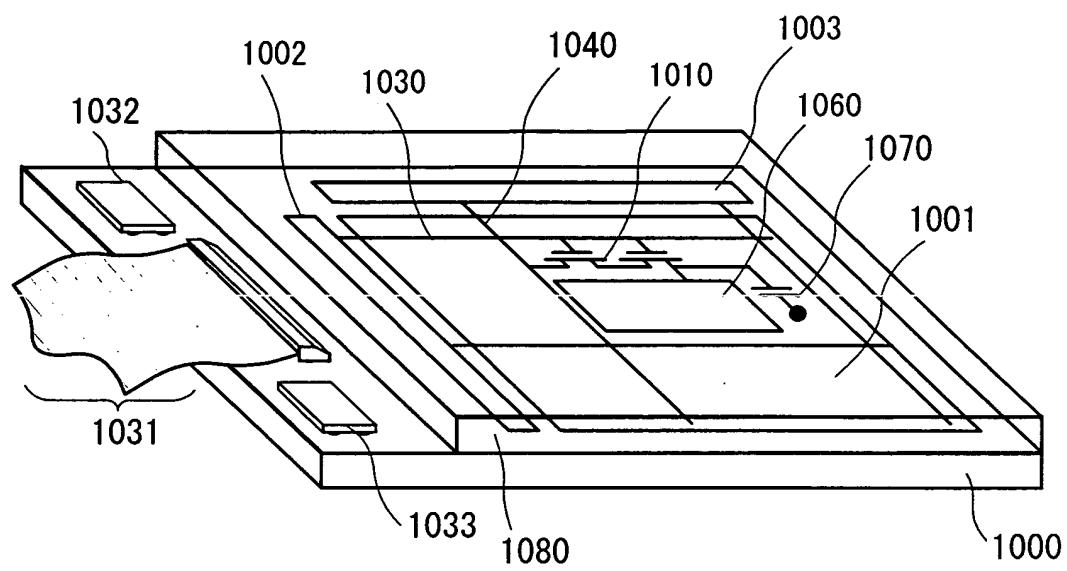


FIG. 12

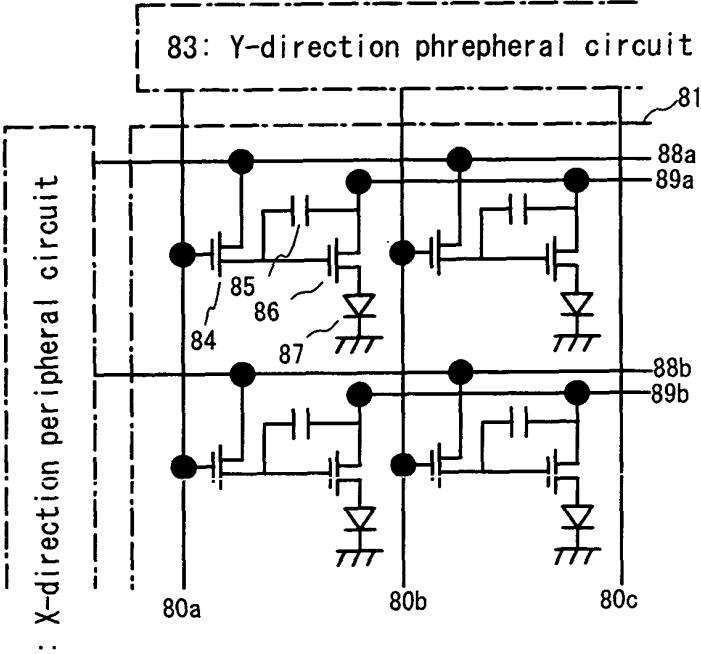


FIG. 13

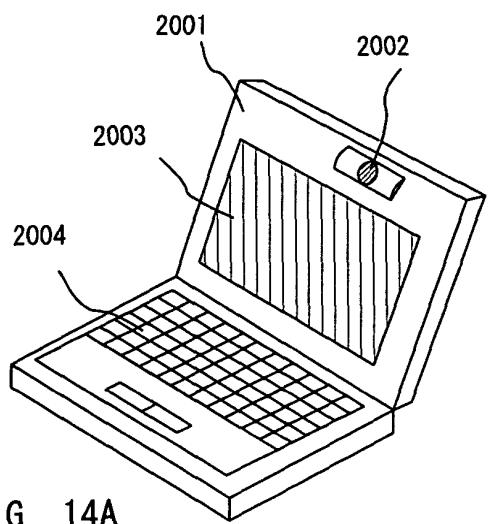


FIG. 14A

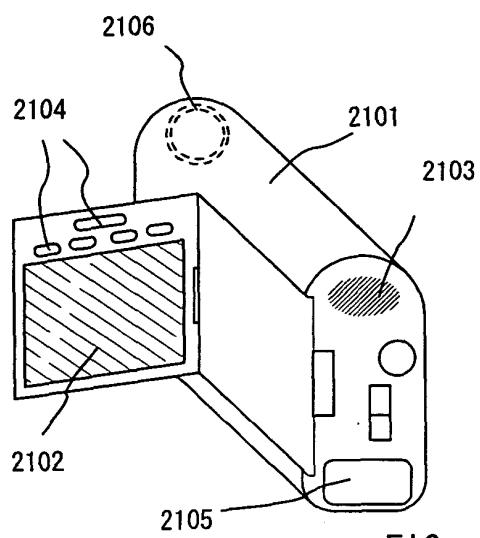


FIG. 14B

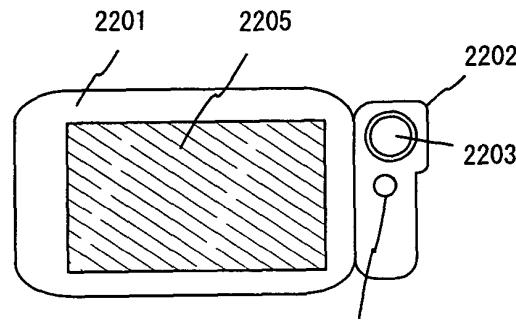


FIG. 14C

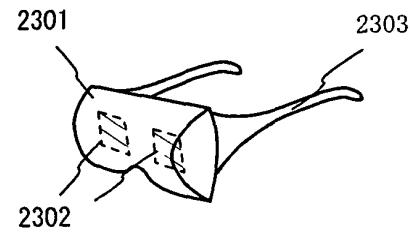


FIG. 14D

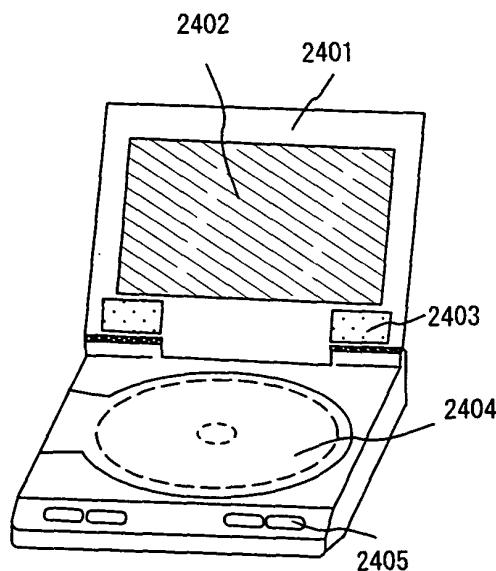


FIG. 14E

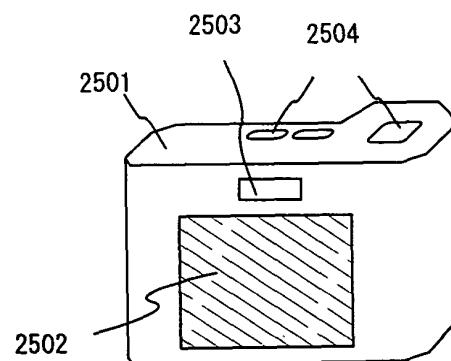


FIG. 14F

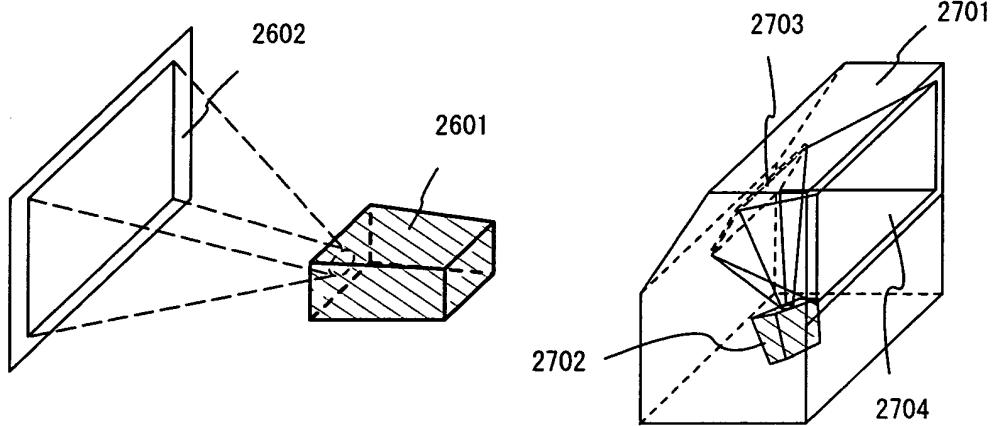


FIG. 15A

FIG. 15B

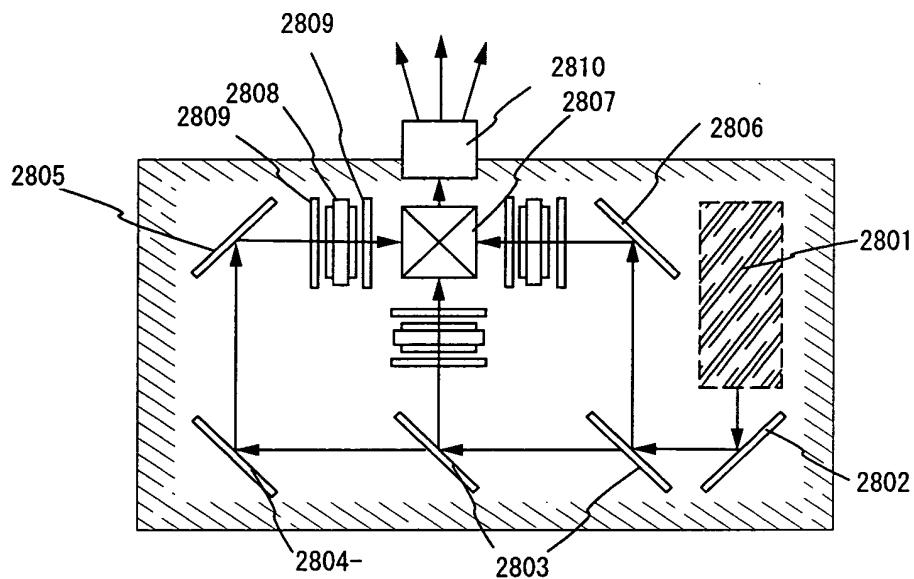


FIG. 15C

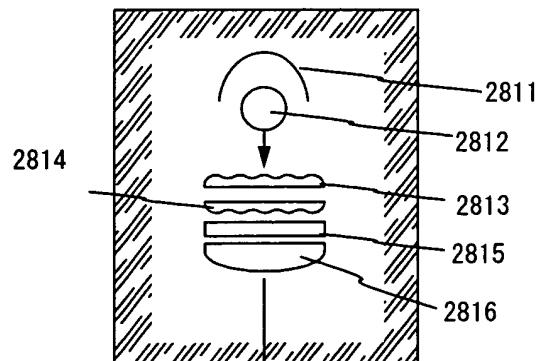


FIG. 15D

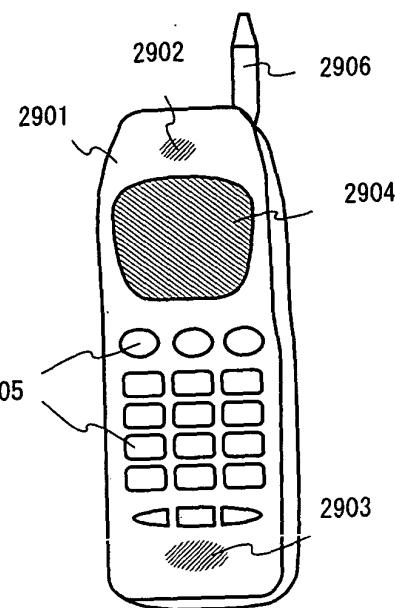


FIG. 16A

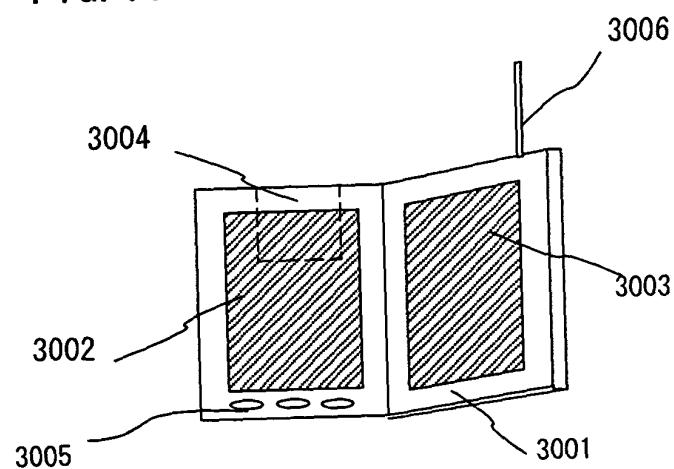


FIG. 16B

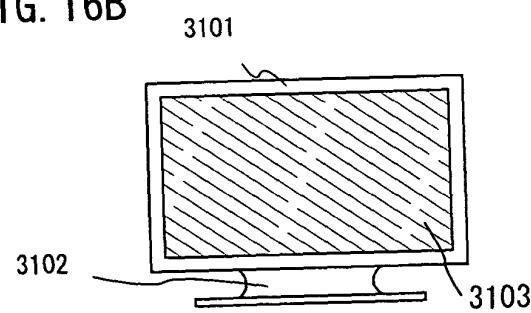


FIG. 16C

000000000000000000000000

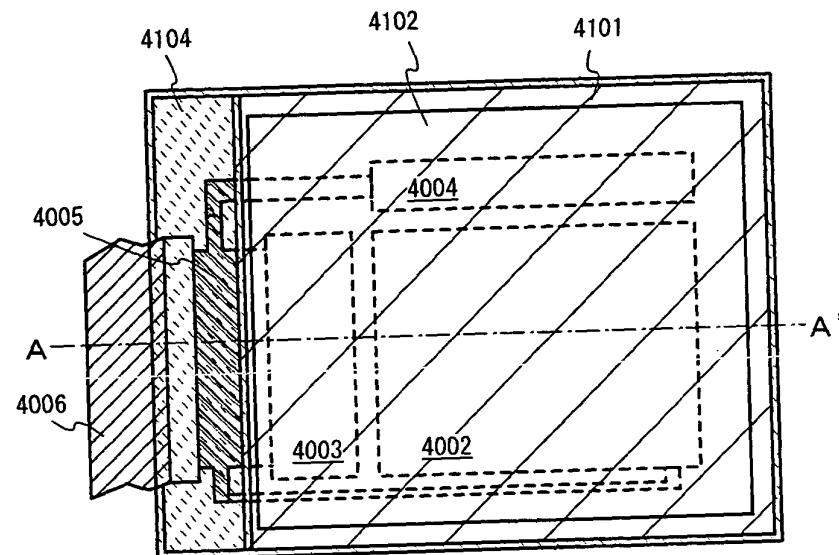


FIG. 17A

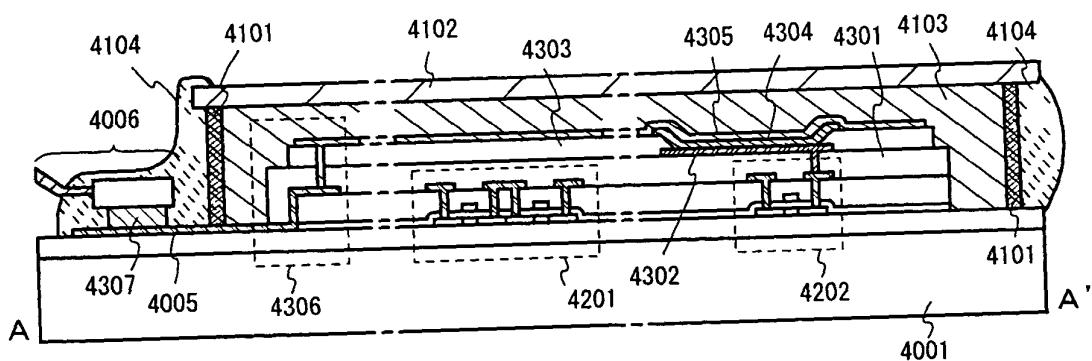


FIG. 17B

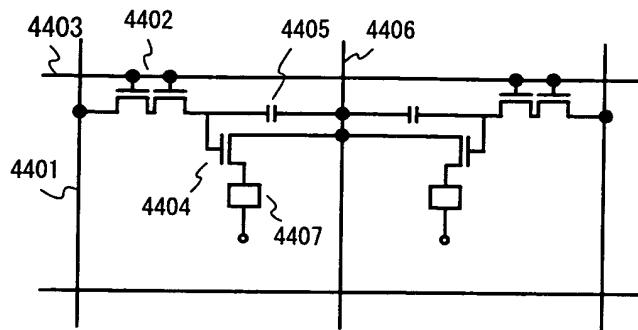


FIG. 18A

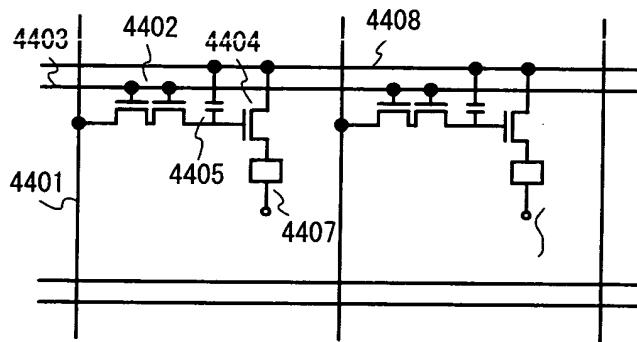


FIG. 18B

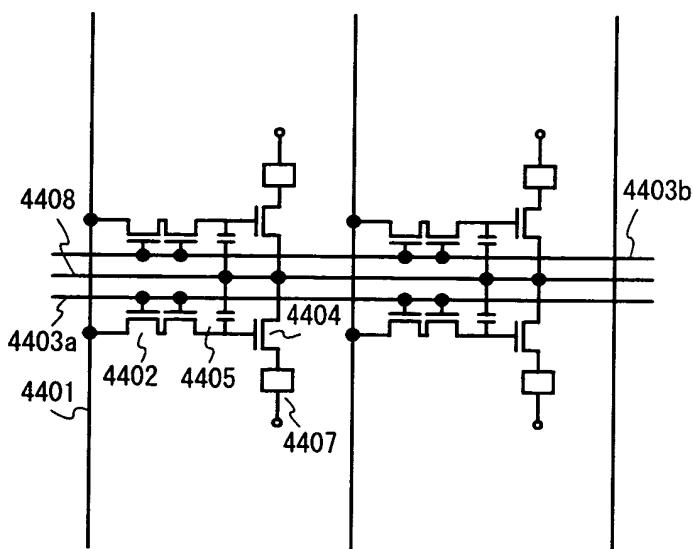


FIG. 18C